

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicants: K. TANAKA, et al.

Serial No.: 10/586,305

Filed: July 14, 2006

For: MANUFACTURING METHOD OF A SEMICONDUCTOR  
DEVICE HAVING ELECTRODES ON OPPOSED SIDES OF AN  
IC ELEMENT THEREOF AND FIRST AND SECOND CIRCUIT  
LAYERS ELECTRICALLY CONNECTED TO THE  
ELECTRODES

Group: 2829

Examiner: Shaun M. Campbell

Conf. No.: 2516

**PETITION FOR EXTENSION OF TIME**

Commissioner for Patents

P.O. Box 1450

May 11, 2010

Alexandria, VA 22313-1450

Sir:

In the matter of the above-identified application, Applicants hereby respectfully petition for an extension of time to permit filing a response within the first month subsequent to expiration of the shortened statutory period set in the outstanding Office Action mailed January 11, 2010. A credit card payment form in the amount of \$130.00 to cover the required fee for the requested extension of time is attached hereto.

It is respectfully requested that any shortage in the fee be charged to the account of Antonelli, Terry, Stout & Kraus, LLP, Account No. 01-2135 (1204.46401X00) and excess fees credited to account.

Respectfully submitted,

/William I. Solomon /

William I. Solomon

Registration No. 28,565

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WIS/kmh  
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